



# Projects & Publications

In reverse chronological order

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Cristiano Rodrigues

2020



- Projects

- [Vermont](#)

- [Unitec](#)

- [Avnet](#)

- [Opto](#)

- [Motorola / Freescale](#)

- Publications

- [TCC FGV](#)

- [IEEE](#)

## Projects at Vermont Representações - 2018 - present



### [Semtech LoRa™](#)

Responsibilities:

- Business Development
- Demand Creation and Market Growth
- Development Commercial Support



### [Flex Power Modules](#)

Responsibilities:

- Demand Creation
- Business Growth
- Commercial Support to Customers and Distributors

Even though working with other Vermont's lines promotion, the focus lines for my focus are presented here.

Besides that, I've been developing a brand work to promote Vermont as a value proposal to customers, exploring synergy among all product lines.



## Projects at Unitec Semicondutores - 2015 to 2018



### USC2131 - SoC Lora™ with ARM core

#### Responsibilities:

- Project viability analysis
- High level specification
- Development monitoring with technical team

Both projects haven't started production due to operational issues in the company.

All developed work was kept available for later utilization.



### USC21122 - SoC Bluetooth™ with ARM core

#### Responsibilities:

- Project viability analysis
- High level specification
- Development monitoring with technical team





During my time at Avnet as Field Applications Engineer, I've supported several projects in many different customers, they can't be listed here.

The product lines for which I've acted as focal points are listed above.





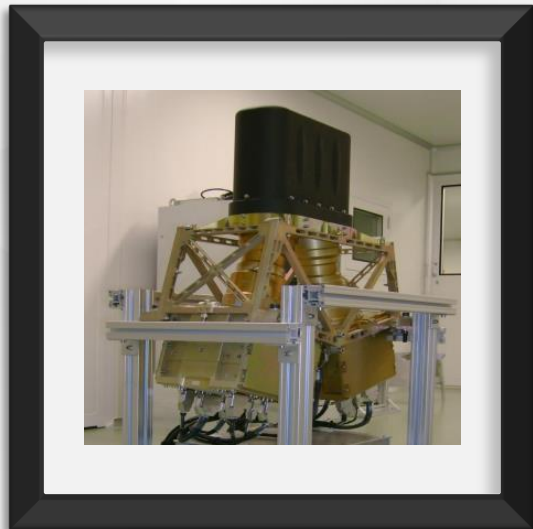
## Projects at Opto Eletrônica (today OpTo Space & Defense) - 2010 to 2011



Thermal Monocle [VDN X1](#) (military usage)

Responsibilities:

- Video processor specification and design
- Design adaptation for usage on soil-soil missile launching engine



Satellite Camera for [INPE PMN AWF1 Amazonia-1](#)

Responsibilities:

- Aerospace FPGA recoding

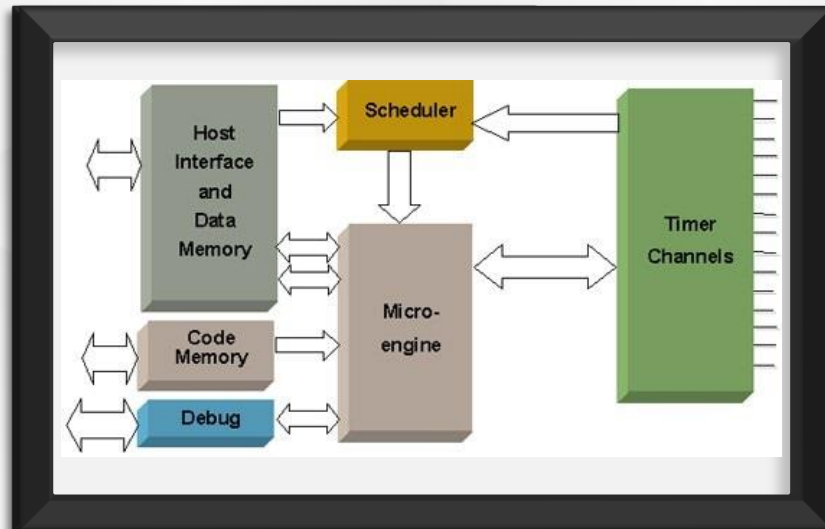
## Projects at Motorola and Freescale (now NXP) - 2001 to 2010



[MPC55xx](#) and [MPC563x](#) families - PowerPC MCUs for automotive applications

Responsibilities:

- Testbenches, integration verification strategy and patterns development



[eTPU](#) (Enhanced Time Processor Unit) - Co-process used in several MCU families

Responsibilities:

- Specification
- Desenvolvimento de ferramentas, testbenches, estratégias para verificação Functional verification patterns, strategy, testbenches and tools development
- Formal verification development and implementation

Papers published on [IEEE LATW 05](#) and [IEEE LATW 09](#)



## Publications

- [GERENCIAMENTO DO RISCO EM PROJETOS NA ATUALIDADE: UMA REVISÃO BIBLIOGRÁFICA](#) - "Risk management on projects nowadays: Notes from a bibliographic review"

Presented in 2008 while finishing Project Management MBA, *lato sensu*, at Fundação Getúlio Vargas as a partial requisite to get specialist degree on Project Management

- [FUNCTIONAL VERIFICATION OF A TIMING CO-PROCESSOR: A CASE STUDY](#)

Published and presented in 2005 at the IEEE LATW

- [A CASE STUDY FOR FORMAL VERIFICATION OF A TIMING CO-PROCESSOR](#)

Published in 2009 at the IEEE LATW





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